



Plug&Play FPGA programming for everybody

24th March 2021 – online

In times of fast evolution, staying at the cutting edge of technology is only achievable by continuous learning. With this seminar, Arrow provides you the chance for a job specific training.

IDEs, tutorials and example code are widely available for microcontrollers. However, it's way harder to get started in the FPGA world.

The mission of VHDPlus is to change that for the better. This is an innovative development environment providing a higher level of abstraction. This simplifies the programming of FPGAs and eases the path from a design idea to a real product. It automatically generates code for your connected hardware that is based on a new programming language. Additionally, tools for processor implementation, software development, simulation and more help with every step of the FPGA development process.

To help beginners, a growing number of video tutorials, open source libraries and examples are available. The seminar gives overview of the IDE and the hardware. You see how to work with the MAX1000 board, VHDPlus/CRUVI hardware, how to add a processor to your project with only a few clicks and how the IDE helps with simulation.

Speakers: Hendrik Mennen (Co-Founder, Protop Solutions)

Language: English
Prerequisites: none

Seminar Actions: Presentation

Contact Person: Helmut Ploetz, hploetz@arroweurope.com

Agenda (Time zone: CET)

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| 10:00 – 10:05 | Welcome | | | |
| 10:05 – 10:20 | Presentation of IDE, VHDPlus/CRUVI Hardware and MAX1000 Integration | | | |
| 10:20 – 11:00 | Demonstration of IDE Read and visualize accelerometer data from MAX1000 Generate project with CRUVI hardware Simulate your project Add a processor to the FPGA and read the CO2 values of the MAXCO2 board | | | |
| 11:00 – 11:20 | Presentation of tutorials, open source IP and the community | | | |
| 11:20 – 11:30 | Questions & Answers | | | |

<u>Register</u>