

Press Release

Design Code Separately and Integrate Seamlessly with Dual-core dsPIC Digital Signal Controller

dsPIC33CH family optimized for high-performance and time-critical, real-world embedded control

CHANDLER, Ariz., June 25, 2018 — System developers designing high-end embedded control applications can benefit from a new Digital Signal Controller (DSC) with two dsPIC® DSC cores in a single chip, now available from Microchip Technology Inc. (**NASDAQ: MCHP**). The dsPIC33CH has one core that is designed to function as a master while the other is designed as a slave. The slave core is useful for executing dedicated, time-critical control code while the master core is busy running the user interface, system monitoring and communications functions, customized for the end application. The dsPIC33CH is designed specifically to facilitate independent code development for each core by separate design teams and allows seamless integration when they are brought together in one chip. For more information visit: www.microchip.com/dsPIC33CH.

The dsPIC33CH family is optimized for high-performance digital power, motor control and other applications requiring sophisticated algorithms. This includes applications such as wireless power, server power supplies, drones and automotive sensors. For example, in a digital power supply, the slave core manages the math-intensive algorithms, while the master core independently manages the PMBus™ protocol stack and provides system monitoring functions, increasing overall system performance and responsiveness. Distributing the overall workload across two DSC cores in a single device enables higher power density through higher switching frequencies, leading to smaller components. The dsPIC33CH family was designed for live updating of the system, which is especially important for power supplies where firmware updates must be made with zero downtime.

In an automotive fan or pump, the slave core is dedicated to managing time-critical speed and torque control while the master manages the Controller Area Network Flexible Data rate (CAN-FD) communications, system monitoring and diagnostics. The two cores work seamlessly together, enabling advanced algorithms to improve efficiency and responsiveness. In addition, each of the new cores in the dsPIC33CH devices has been designed to provide more performance than current dsPIC DSC cores through: 1) more context-selected registers to improve interrupt responsiveness; 2) new instructions to accelerate Digital Signal Processor (DSP) performance; and 3) faster instruction execution.

“Customers tell us one of their biggest challenges is integrating software from multiple teams where one team is focused on the time-critical control code and another is working on the rest of the application,” said Joe Thomsen, vice president of Microchip’s MCU16 business unit. “We created this dual-core product to simplify that software integration and optimize the performance for math-intensive applications.”

The dsPIC33CH family delivers unprecedented integration in a small 5 x 5 mm package and includes features such as CAN-FD communications. To reduce system costs and board size, advanced peripherals are available to each core including high-speed ADCs, DACs with waveform generation, analog comparators, analog programmable gain amplifiers and high-resolution Pulse Width Modulation (PWM) hardware. Having two cores, with dedicated peripherals, allows the cores to be programmed to monitor each other for functional safety reasons, facilitating robust system design.

For additional information, contact any Microchip sales representative or authorized worldwide distributor, or visit Microchip's website. To purchase products mentioned in this press release, go to Microchip's full-service channel **microchipDIRECT** or contact one of Microchip's authorized distribution partners.

Development Support

The dsPIC33CH is supported by Microchip's MPLAB® development ecosystem including Microchip's free, downloadable and award-winning MPLAB X Integrated Development Environment (IDE) and MPLAB Code Configurator.

The dsPIC33CH Curiosity Board (DM330028) is a cost-effective and flexible platform, enabling customers to rapidly create a feature-rich prototype. The dsPIC33CH Plug-in Module (PIM) for motor control platforms (MA330039) is available for Microchip's MCLV-2 and MCHV-2/3 systems. The dsPIC33CH PIM for general purpose platforms (MA330040) is now available for the Explorer 16/32 development board (DM240001-2).

Pricing and Availability

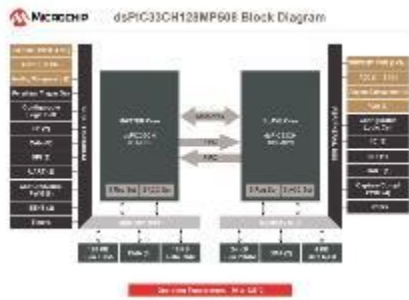
The dsPIC33CH is available in eight package variants. The 28-pin dsPIC33CH64MP202 is priced at less than \$2.00 each in high volume. Variants include packages from 28 to 80 pins and as small as 5 x 5 mm. Memory sizes range from 64 to 128 KB of Flash.

The dsPIC33CH Curiosity development board is available now for \$34.99 each.

The dsPIC33CH PIM for motor control development boards is available now for \$25.00 each.

The dsPIC33CH PIM for use with Explorer 16/32 boards is available now for \$25.00 each.

Images



Microchip Technology Inc. (NASDAQ: MCHP) is a leading provider of microcontroller, mixed-signal, analog and Flash-IP solutions, providing low-risk product development, lower total system cost and faster time to market for thousands of diverse customer applications worldwide. Headquartered in Chandler, Arizona, Microchip offers outstanding technical support along with dependable delivery and quality. For more information, visit the Microchip website at www.microchip.com.

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