

Arria 10 GX Dual FPGA Board

2x 20nm FPGAs with

1.5 million LEs

Introducing ground-breaking single precision floating point performance of up to 1.5 TFLOPS per device. The BittWare 510T Datacenter Co-Processor is a standard-height, dual-slot PCIe board designed to deliver fast and efficient performance per watt. The OpenCL-programmable 510T features two Intel Arria 10 FPGAs, along with four banks of DDR4 external memory per FPGA.

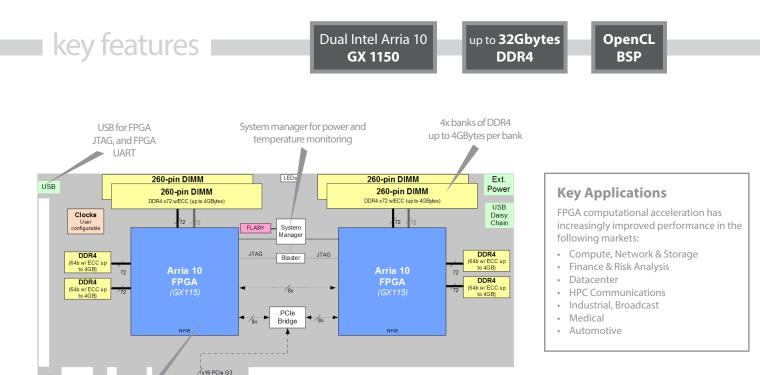
Gen3 x16 PCle

Tool Flow Flexibility for Softwareor Hardware-Based Development



FPGA Tools

- OpenCL support for softwareorientated customers
- · Abstration for faster development
- Push-button flow for FPGA executable, driver, and API
- Add optimized HDL IP cores to OpenCL designs as libraries
- Traditional VHDL/Verilog support for hardware-orientated customers
- · Hand-code for ultimate performance
- · High-Level Synthesis (HLS) available for rapid development
- FPGA card designed to support standard Intel IP cores for Stratix 10



Additional Services

Take advantage of BittWare's range of design, integration, and support options





CustomizationServer IntegrationAdditional specification options
or accessory boards to meet
your exact needs.Available pre-integrated
in our TeraBox servers in a
range of configurations.

Application Benchmark Report	~
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Application Optimization Ask about our services to help you port, optimize, and benchmark

your application.



Service and Support BittWare Developer Site provides online documentation and issue tracking.

Board Specifications

FPGAs	 Two Intel Arria 10 GX 1150GX in F45 package Core speed grade -2: I/O speed grade -3 Contact BittWare for other Arria 10 GX options
On-board Flash	Flash memory for booting FPGA
On-board memory	 Eight banks of DDR4 SDRAM x 72 bits Four 4GB banks per FPGA (32GB total) Transfer rate: 2133 MT/s
Host interface	• x16 Gen3 interface
System manager	 On-board Intel USB Blaster II Power and temperature monitoring Fault condition reporting to FPGA
Cooling	 Standard: double-width active heatsink (embedded fan) Optional: double-width passive heatsink
Electrical	 On-board power derived from 12V PCIe slot & one AUX connectors (8-pin) Power dissipation is application dependent Typical max power consumption 225W
Environmental	 Operating temperature: 5℃ to 35℃

Quality	Manufactured to ISO9001:2008 IPC-A-610-ClassRoHS compliant
Form factor	 Standard-height PCIe dual-slot board 4.37 x 10.5 inches (106.65 x 266.7 mm)

Development Tools

FPGA development	BIST - Built-In Self-Test for CentOS 7 provided with source code (pinout, gateware, PCIe driver & host test application)
Application development	Supported design flows - Intel FPGA OpenCL SDK, Quartus Prime Pro (HDL, Verilog, VHDL, etc.)

Deliverables

510T FPGA board

- USB cable (front panel access)
- Built-In Self-Test (BIST)
- OpenCL HPC Board Support Package (BSP)
- 1-year access to online Developer Site
- 1-year hardware warranty





To learn more, visit www.BittWare.com

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